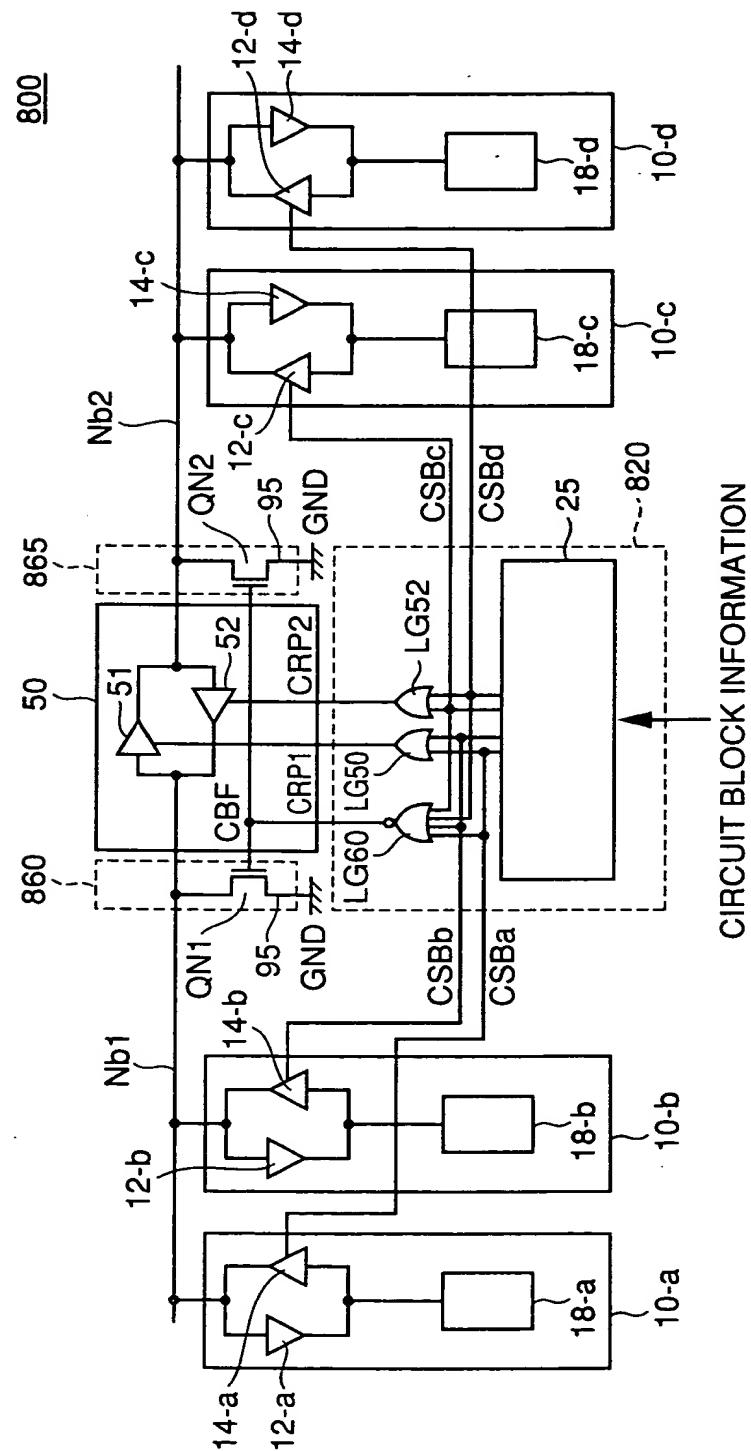


FIG. 1



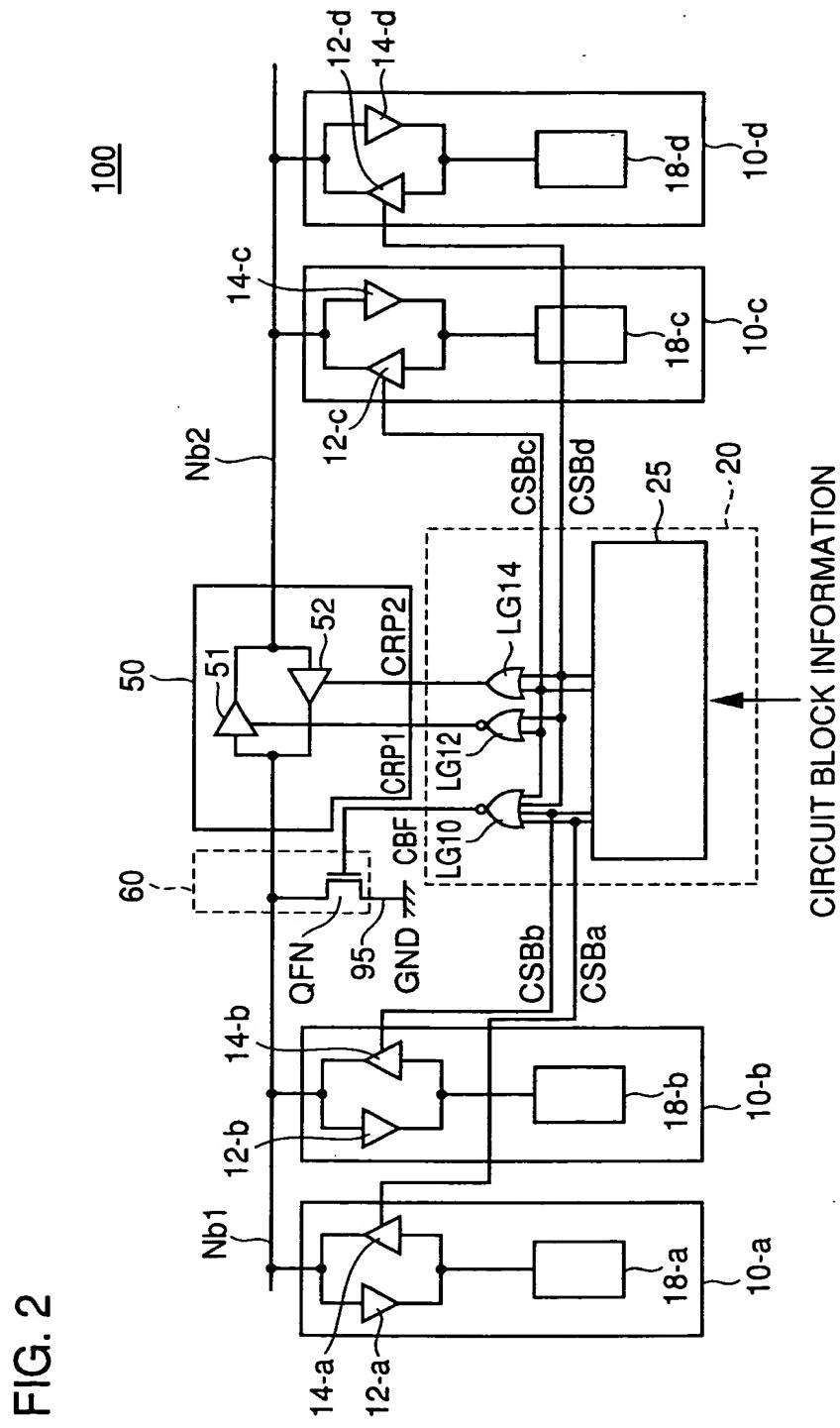


FIG. 3

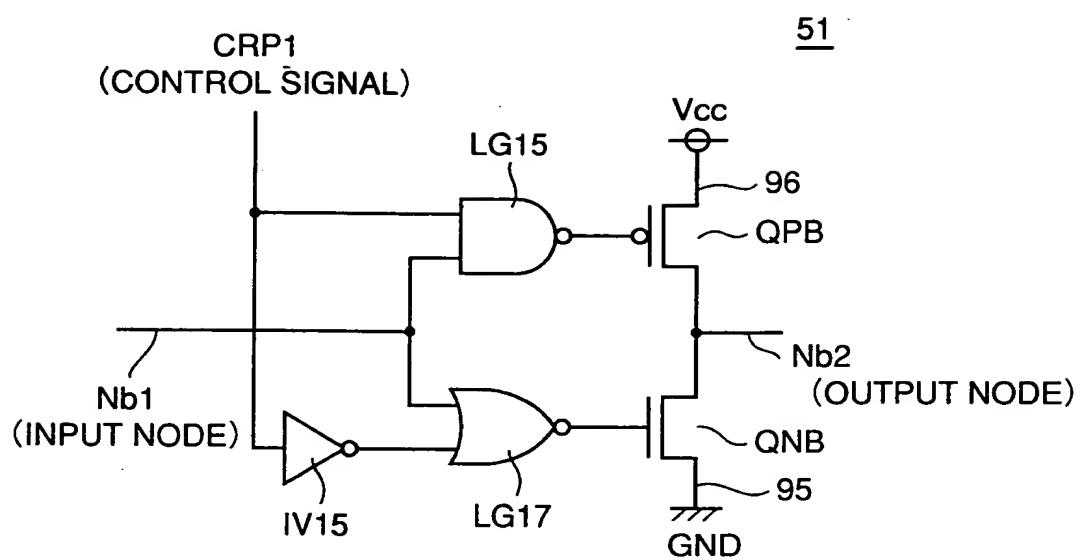


FIG. 4

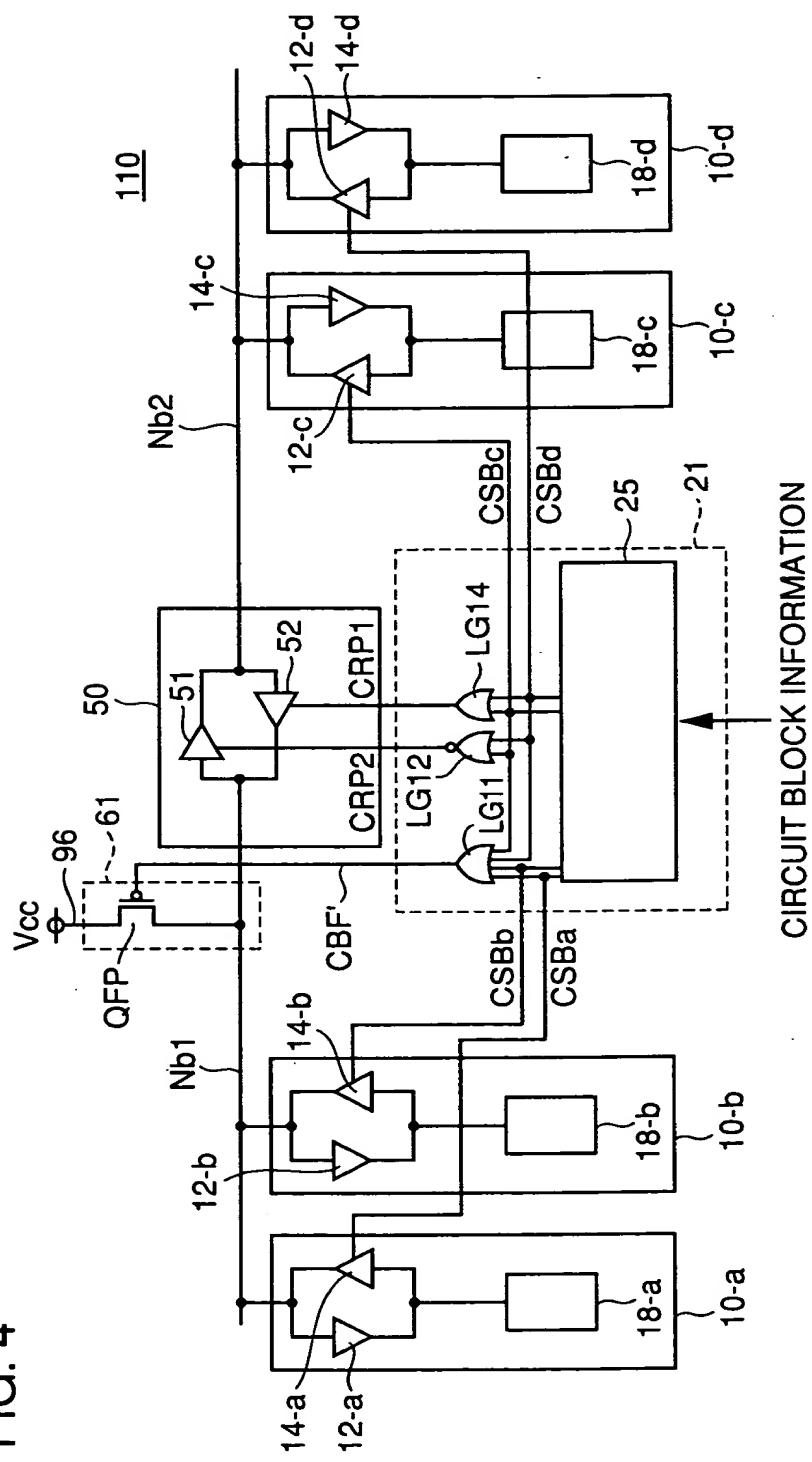


FIG. 5

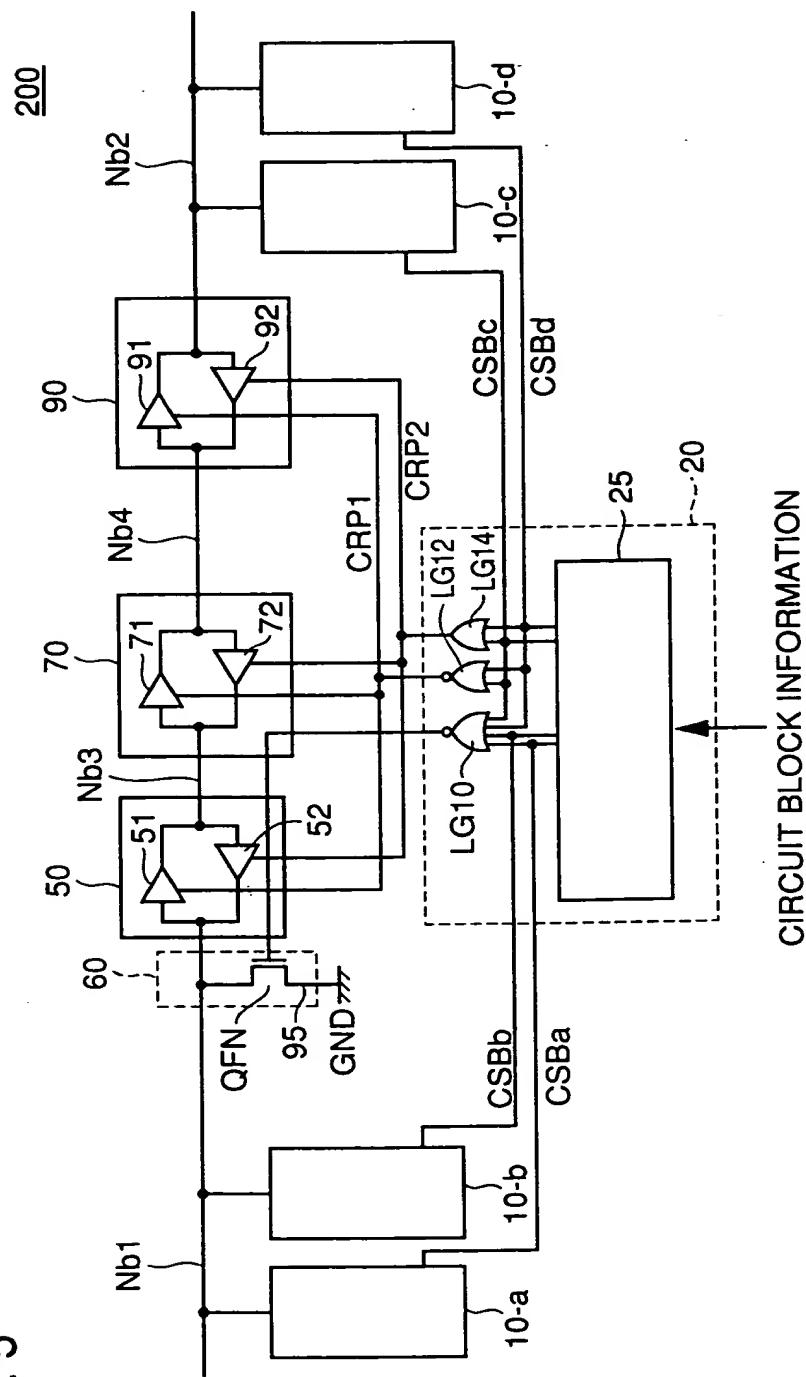
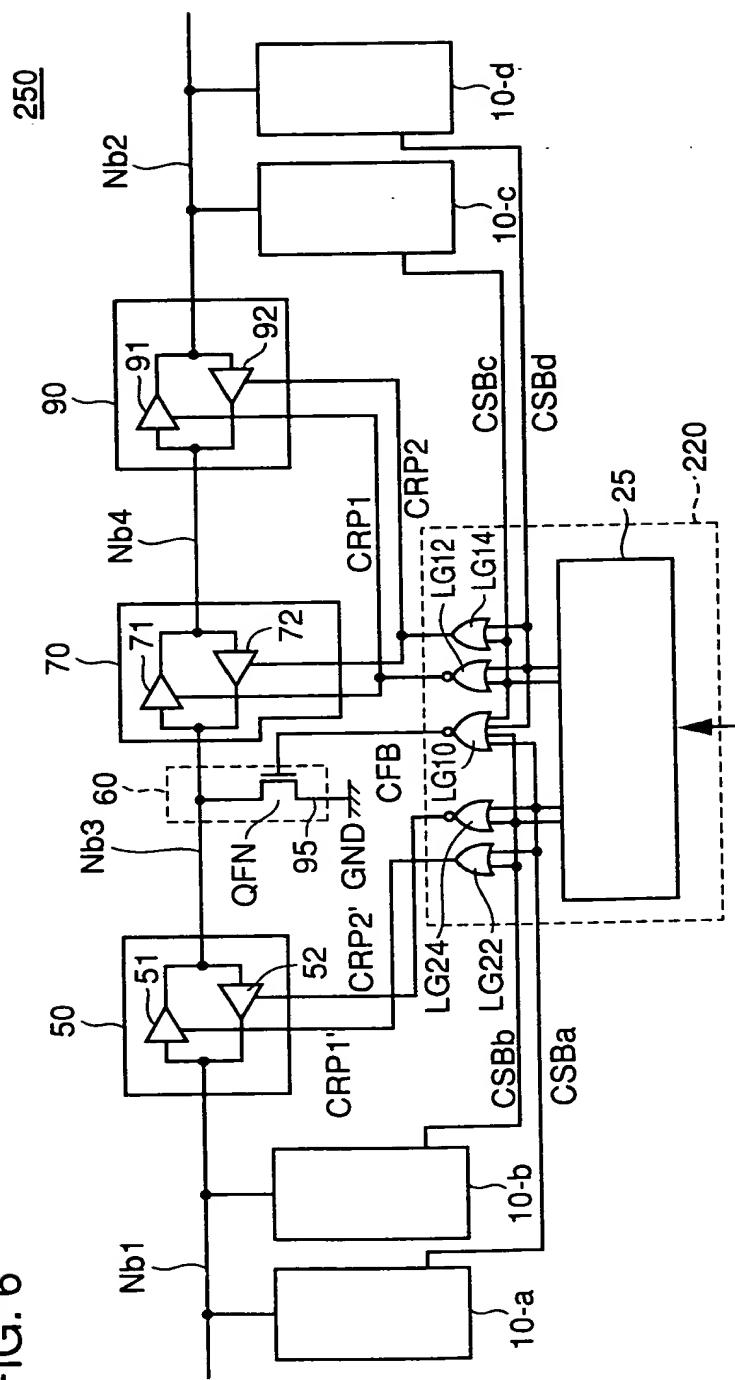


FIG. 6



CIRCUIT BLOCK INFORMATION

FIG. 7

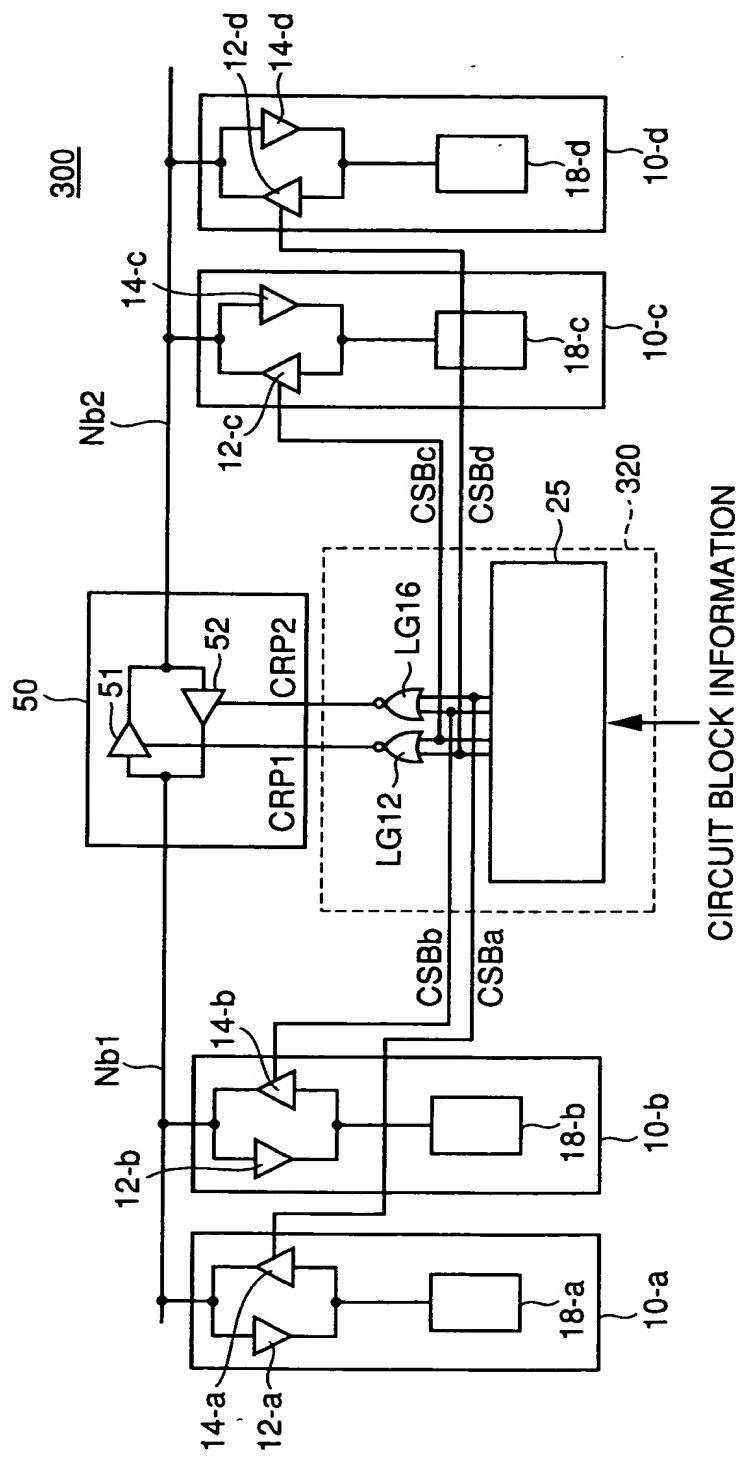


FIG. 8

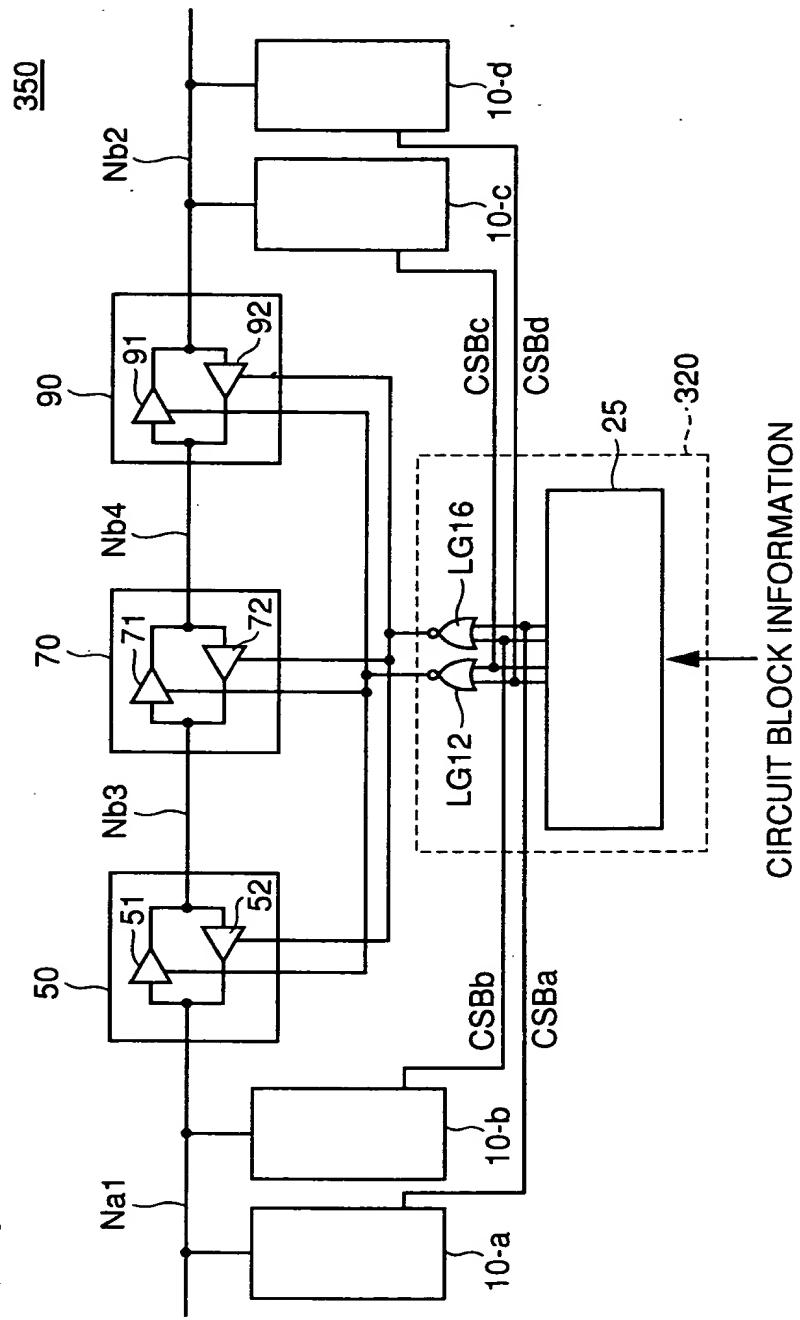


FIG. 9 PRIOR ART

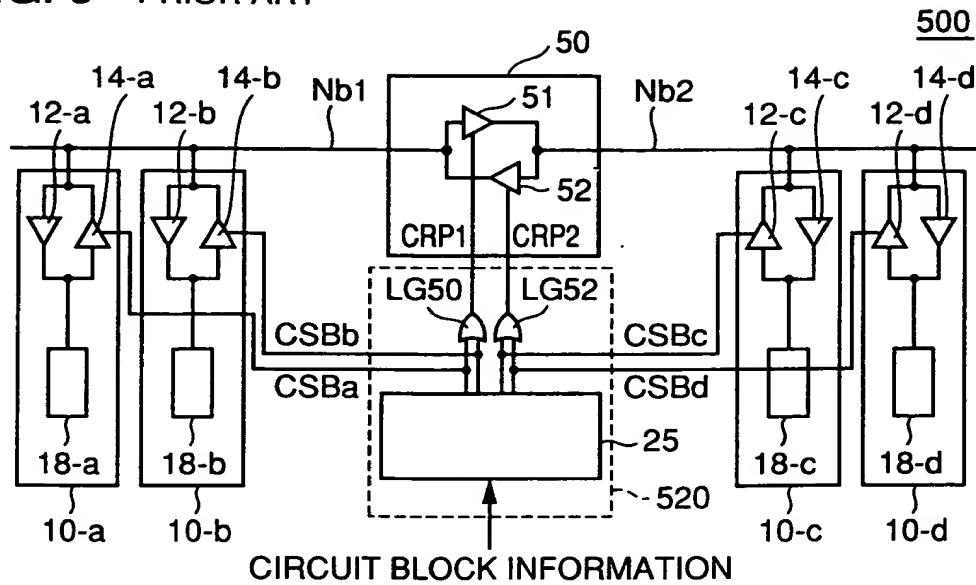


FIG. 10 PRIOR ART

